

REMARKS/ARGUMENTS

Claims 1-24 and 26-29 were pending in this application when last examined by the Examiner. Claims 2-4, 6, 8-9, 12, 17-, 20, and 23 have been amended. Claims 1, 7, 22, and 26-29 have been canceled. Claims 30-31 have been added. The amendments find full support in the original specification, claims, and drawings. No new matter has been added. In view of the above amendments and remarks that follow, reconsideration and an early indication of allowance of the now-pending claims 2-6, 8-21, 23-24, and 30-31 are respectfully requested.

As an initial matter, Applicant brings to the Examiner's attention that an Information Disclosure Statement (IDS) and accompanying FORM PTO/SB/08A/B were submitted on August 12, 2008. Applicant respectfully requests that an initialed copy of said FORM PTO/SB/08A/B be entered in the application file and returned to Applicant with the next communication from the Office in accordance with MPEP § 609.

In addition, Applicant notes that the USPTO records still reflect a date of January 29, 2004, as the filing date for the present application. As indicated in the Decision Granting Petition that was submitted with Applicant's last Amendment dated February 22, 2008, the correct filing date for this application is January 30, 2004. Accordingly, Applicant respectfully requests that the USPTO records be updated to reflect the correct filing date.

Claims 1-3 and 26-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over "RIFLE-62: a flexible environment for prototyping dynamically reconfigurable systems" by Milan Vasilko and David Long in view of Sample (U.S. Patent No. 5,943,490). Claims 4-8 are rejected under 35 U.S.C. 103(a) as being unpatentable over Vasilko in view of Sample and in further view of Iannotti (U.S. Patent No. 6,356,823). Claims 9-19, 22-24, and 29 are rejected under 35 U.S.C. 103(a) as being unpatentable over Vasilko in view of Sample and in further view of Carmichael (U.S. Patent No. 5,864,712). Claims 20 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Vasilko in view of Sample and in further view of Parvahan (U.S. Patent No. 5,908,455). Applicant respectfully traverses these rejections.

Claim 2, as amended, now recites "an internal memory in said computing block coupled to said bus controller over a first internal bus," where "said first communication block and each of said one or more devices transfer data to or from each other according to two transfer techniques." Claim 1 then recites that "a first one of the two transfer techniques transfers a first portion of the data by way of said PCI bus, bus controller, and the second internal bus without using said internal memory, the first one of the two transfer techniques being invoked for acquiring input information on the mother board during a first action of an application," and that "a second one of the two transfer techniques transfers a second portion of the data via said internal memory, wherein the second portion of the data from one of said one or more devices is stored in said internal memory via said first internal bus and said bus controller, and the second portion of the data stored in said internal memory is transmitted to said first communication block via said bus controller concurrently, wherein the second transfer technique is carried out before a second action of the application responsive to an interrupt request from the core board to the mother board." (Emphasis added). Support for these limitations may be found in various portions of Applicant's application, including for example, paragraphs 0077-0081, 0092, and 0131 of Applicant's specification. Neither Vasilko nor Sample teach or suggest all of the limitations of amended claim 2.

Assuming *arguendo* that the RIFLE-62 board disclosed by Vasilko is the claimed "core board," the RIFLE-62 board does not include the claimed "internal memory," and does not use the "two transfers techniques," where use of this "internal memory" is withheld for "a first one of the two transfer techniques," but used to store data "in a second one of the two transfer techniques . . . carried out before a second action of the application responsive to an interrupt request from the core board to the mother board."

Sample fails to make up for the deficiencies in Vasilko. Even if Sample's control board 600 or logic or I/O board 200, 300 could be deemed to be the claimed "core board," these boards also fail to include the claimed "internal memory" and Sample fails to teach or suggest the claimed "two transfer techniques" for either one of the boards. Accordingly, claim 2 is now in condition for allowance.

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Independent claim 23 includes limitations similar to the limitations of claim 2 which make claim 2 allowable. Accordingly, claim 23 is in condition for allowance for reasons similar to claim 2.

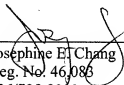
Claims 3-6, 8-21, and 24 are also in condition for allowance because they depend on an allowable base claim, and for the additional limitations that they contain.

Claims 30-31 are new in this application. Claims 30-31 are in condition for allowance because they depend on an allowable base claim, and for the additional limitations that they contain.

In view of the above amendments and remarks, reconsideration and an early indication of allowance of the now-pending claims 2-6, 8-21, 23-24, and 30-31 are respectfully requested.

Respectfully submitted,
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